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A OPM N N MDKOD I

±6% Frequency
Spread Spectrum

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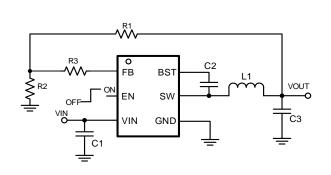
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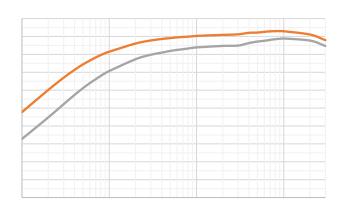
KKGD OD I N

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OTKD G KKGD OD I







M DND I CDNO MT

D M MDAMH ODI

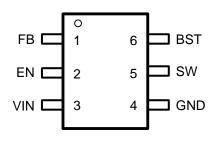
I: KM GNF K	I: D: F: KDEG	I: D: BLKBIMBHG
1)		

2N GPO H SDHPH M OD BN

Over operating free-air temperature unless otherwise noted⁽¹⁾

DESCRIPTION	MIN	MAX	UNIT
BST	-0.3	48	V
VIN, SW, EN	-0.3	42	V
FB	-0.3	5.5	V
Operating junction temperature ⁽²⁾	-40	125	°C
Storage temperature T _{STG}	-65	150	°C

KD I ADBPM OD I



Top View: TSOT23-6L, Plastic

(1)

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KDI API ODIN

G: F	GH	I BG NG MBHG
FB	1	Buck converter output feedback sensing voltage. Connect a resistor divider from VOUT to FB to set up output voltage. The device regulates FB to the internal reference of 0.8V typical.
EN	2	Enable logic input. Floating the pin enables the device. This pin supports high voltage input up to VIN supply to be connected VIN directly to enable the device automatically. The device has precision enable thresholds 1.18V rising / 1.1V falling for programmable UVLO threshold and hysteresis.
VIN	3	Power supply input. Must be locally bypassed.
GND	4	Power ground. Must be soldered directly to ground plane.
SW	5	Switching node of the buck converter.
BST	6	Power supply for the high-side power MOSFET gate driver. Must connect a 0.1uF or greater ceramic capacitor between BST pin and SW node.



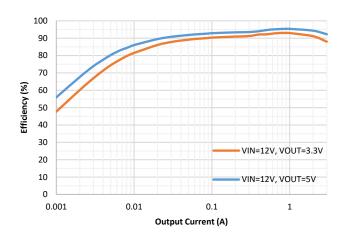
M HH I	K M OD B I DOD IN			
I: K: F M K	EGE/EH G	F BG	F: Q	NGBM
N M OD B	V			
I: K: F M K	EGEVEH G	FBG	F: Q	NGBM
(1) (2)				
OC MH GD	A MH OD I			
I: K: F M K	MA KF: EF MKB	MLHM	. /E	NGBM

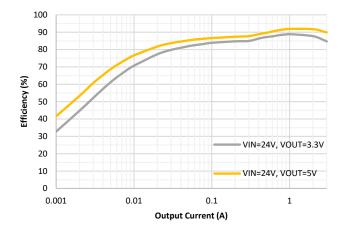


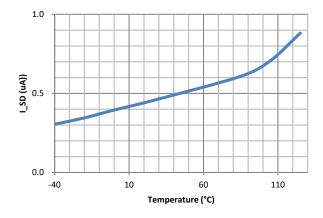
LRF HE	I: K: F M K	M LM HG BMBHG	FBG	MRI	F: Q	NGBM
E						
L						ı
L L M						ı
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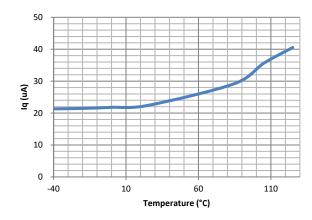


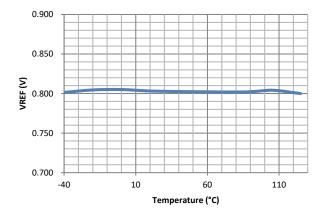
OTKD G C M O MDNOD N

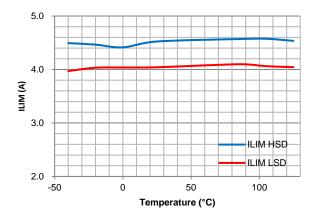






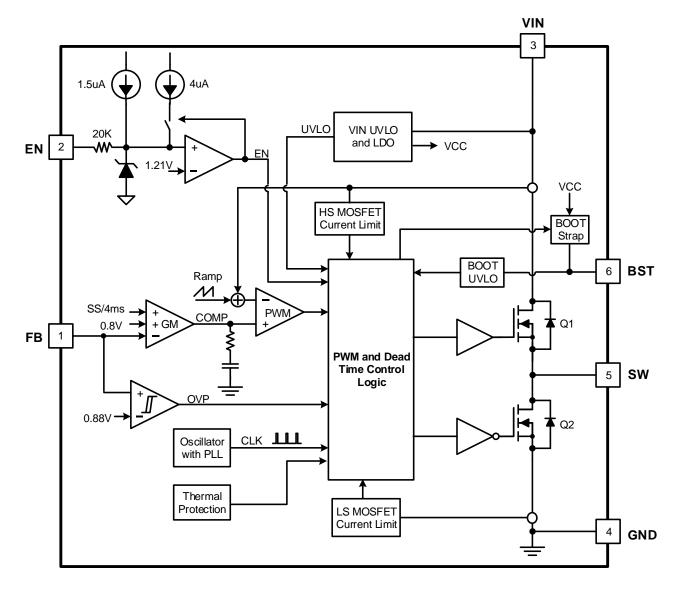








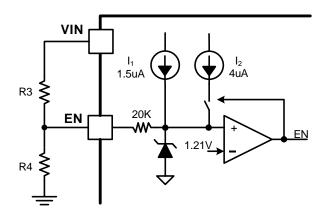
API ODI G2G F DBM H





K M OD I Н OBG I NOEH N 0 Ε L

SCT

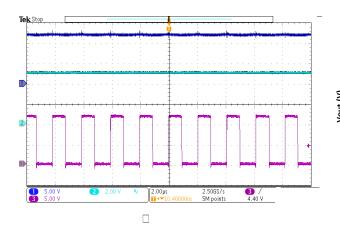


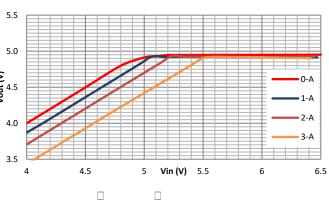
$$R3 = \frac{V_{Start} \left(\frac{V_{ENF}}{V_{ENR}}\right) - V_{Stop}}{I_1 \left(1 - \frac{V_{ENF}}{V_{ENR}}\right) + I_2} \tag{1}$$

$$R4 = \frac{R_3 \times V_{ENF}}{V_{Stop} - V_{ENF} + R_3(I_1 + I_2)}$$
 (2)

FBK L L G K

SCT





I E A F

H O I F H

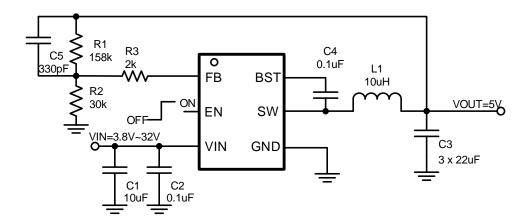
ILFP F

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E K

KKGD OD I DA MH OD I

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Design Parameters	Example Value		
Input Voltage	24V		
Output Voltage	5V		
Output Current	3A		
Output voltage ripple (peak to peak)	±0.3V		
Switching Frequency	400kHz		



B L



H L

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$$C_{OUT} = \frac{\Delta I_{LPP}}{8 \times V_{OUTRipple} \times f_{SW}} \tag{6}$$

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$$R_{ESR} = \frac{V_{OUTRipple}}{\Delta I_{LPP}} \tag{7}$$

 $\frac{1}{10} \times f_{SW}$

$$C_{OUT} > \frac{18k \times G_M \times G_{MP} \times 0.8V}{2\pi \times V_{OUT} \times f_C}$$
 (8)

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 $I_{COUTRMS} = \frac{V_{OUT} \cdot (V_{IN} - V_{OUT})}{\sqrt{12} \cdot V_{IN} \cdot L_{IND} \cdot f_{SW}}$ $\tag{9}$



Н L

$$C_f = \frac{1}{2\pi \cdot f_C \times R_1} \tag{10}$$

Н

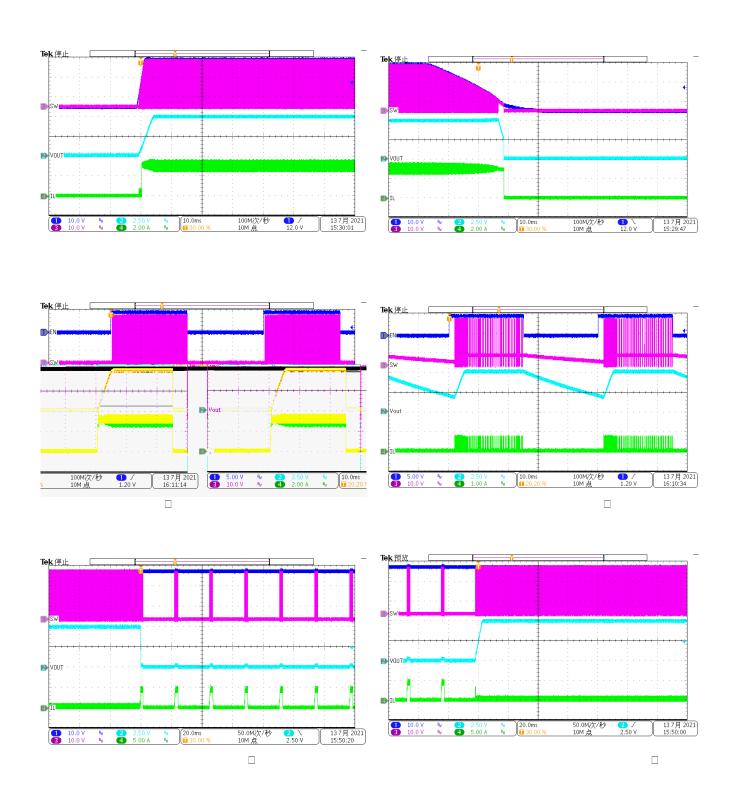
 $R_1 = \frac{(V_{OUT} - V_{ref}) \times R_2}{V_{ref}}$ (11)

M K

Vout	L1	COUT	R1	R2	R3	Cf
3.3V	6.5uH	3*22uF	93.5k	30k		330p
5V	10uH	3*22uF	158k	30k	2k	330p
12V	22uH	3*22uF	422k	30k	2k	330p

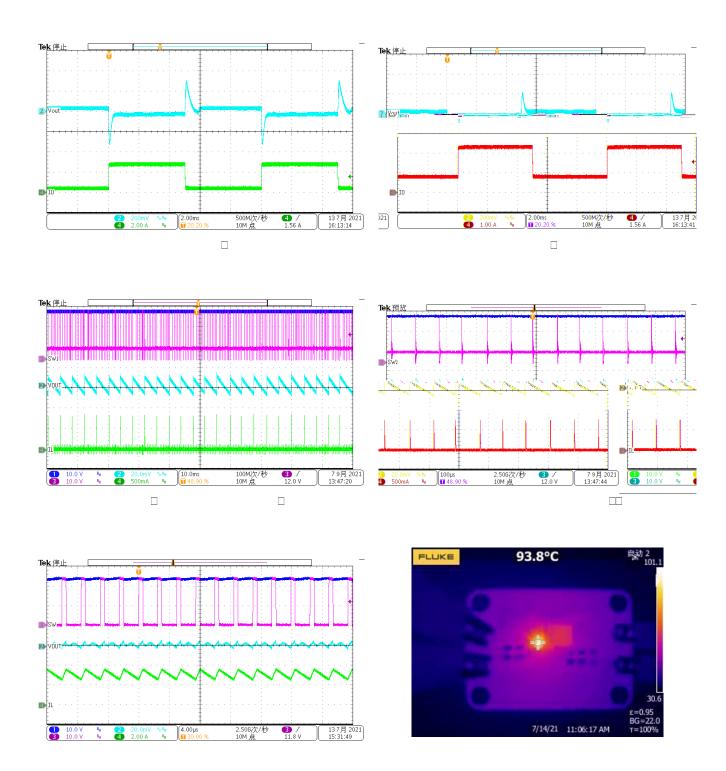


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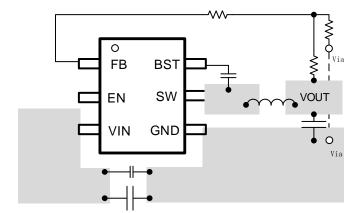


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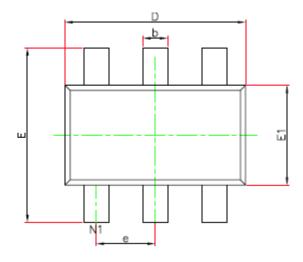


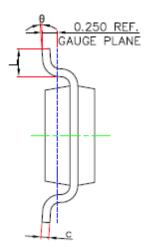
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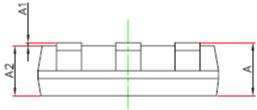




K F B D A MH OD I







GHM 3

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6.

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CVMPOL	Unit: Millimeter			
SYMBOL	MIN	TYP	MAX	
Α			1.10	
A1	0.000		0.10	
A2	0.70		1.00	
D	2.85		2.95	
Е	2.65		2.95	
E1	1.55		1.65	
b	0.30		0.50	
С	0.08		0.20	
е	0.95(BSC)			
L	0.30		0.60	
	0°		8°	



OK I M GDAMH ODI

